

031504

14230 U.S. PTO

FORM PTO-1082

81754.0110
Express Mail Label No. EV 324 112 398 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re application of:
Takayoshi OBINATA of Nagano-ken, Japan
Serial No: Not assigned
Filed: March 15, 2004
For: Semiconductor Wafer, Semiconductor Device and Method for Manufacturing Same, Circuit Board, and Electronic Apparatus

22390 U.S. PTO
10/801107
031504

Mail Stop PATENT APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

CORRESPONDENCE ADDRESS: ☒ Customer Number **000026021**

Transmitted herewith for filing is the patent application identified above.

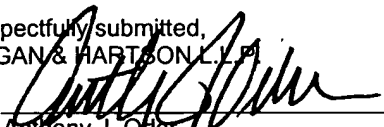
- ☒ 7 sheet(s) of drawings (☒ formal ☐ informal) is(are) enclosed.
- ☒ 22 page(s) of specification and 1 page(s) of abstract of the invention are enclosed.
- ☒ An assignment of the invention to SEIKO EPSON CORPORATION ☐ is enclosed ☒ will follow.
- ☐ An associate power of attorney ☐ is enclosed ☐ will follow.
- ☐ Applicant claims small entity status. See 37 CFR 1.27.
- ☒ Declaration and Power of Attorney ☐ is enclosed ☒ will follow.
- ☒ A certified copy of Japanese Patent Application No. 2003-078097 filed March 20, 2003 from which priority is claimed under 35 U.S.C. § 119 is enclosed.
- ☒ IDS enclosed (☒ with 1 reference(s)).
- ☐ Preliminary Amendment is enclosed.
- ☐ Application data sheet is enclosed. See 37 CFR 1.76.
- ☒ Return postcard is enclosed.

| CALCULATION OF FEES | | | | | | | | | |
|----------------------------------------|--------------------------------------------------------------------|---------------------|-----|-------------------------|---------------------|------------------------------------------------------|-----------|--------|-------|
| ITEM | | TOTAL NO. OF CLAIMS | | NO. OF CLAIMS OVER BASE | LG/SM \$ ENTITY FEE | | \$ AMOUNT | \$ FEE | |
| A | TOTAL CLAIMS FEE | 23 | -20 | 3 | LG=\$18 SM=\$9 | \$18 | 54 | | |
| B | INDEPENDENT CLAIMS FEE* | 3 | -3 | 0 | LG=\$86 SM=\$43 | \$86 | \$ 0 | | |
| C | SUBTOTAL - ADDITIONAL CLAIMS FEE (ADD FINAL COLUMN IN LINES A + B) | | | | | | | 54 | |
| D | MULTIPLE-DEPENDENT CLAIMS FEE | | | | | LARGE ENTITY FEE = \$290 SMALL ENTITY FEE = \$145 | | \$ 0 | |
| E | BASIC FEE | | | | | LARGE ENTITY FEE = \$770 SMALL ENTITY FEE = \$385 | | \$ 770 | |
| F | TOTAL FILING FEE (ADD TOTALS FOR LINES C, D, AND E) | | | | | | | \$ 824 | |
| G | ASSIGNMENT RECORDING FEE | | | | | | | \$ 40 | \$ 40 |
| *LIST INDEPENDENT CLAIMS 1, 12 and 18. | | | | | | | | | |

- ☐ Please charge my Deposit Account No. 50-1314 the amount of \$ 0. A copy of this letter is enclosed.
- ☐ A check in the amount of \$ 0 to cover the filing fee is enclosed.
- ☐ A check in the amount of \$ 40.00 to cover Assignment Recordation fee is enclosed.
- ☐ The Commissioner is hereby authorized to charge any deficiency for the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-1314. **A copy of this sheet is enclosed.**
 - ☐ Any additional filing fees required under 37 C.F.R. 1.16
 - ☐ Any patent application processing fees under 37 C.F.R. 1.17

Date: March 15, 2004

500 South Grand Avenue, Suite 1900
Los Angeles, CA 90071
Telephone: (213) 337-6700
Facsimile: (213) 337-6701

Respectfully submitted,
HOGAN & HARTSON LLP
By: 
Anthony J. Olier
Registration No. 41,232
Attorney for Applicant(s)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Takayoshi OBINATA

Serial No: Not assigned

Filed: March 15, 2004

For: Semiconductor Wafer, Semiconductor Device and Method for
Manufacturing Same, Circuit Board, and Electronic Apparatus

Art Unit: Not assigned

Examiner: Not assigned

CERTIFICATE OF MAILING VIA U.S. EXPRESS MAIL

"Express Mail" Mailing Label No. EV 324 112 398 US

Date of Deposit: March 15, 2004

Mail Stop PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

I hereby certify that

- ☒ two copies of a letter of transmittal
- ☒ patent application (22 page(s) of specification; 23 claim(s); 1 page(s) of abstract
- ☒ 7 sheet(s) of formal drawings
- ☒ certified copy of Japanese patent application No. 2003-078097 which was filed
March 20, 2003 from which priority is claimed in the subject case pursuant to 35 U.S.C.
§ 119
- ☒ Information Disclosure Statement with 1 reference
- ☒ return postcard

are being deposited with the United States Postal Service "Express Mail Post Office to Addressee"
service with sufficient postage under 37 C.F.R. § 1.10 on the date indicated above and are addressed to:

Mail Stop PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

Arlington, VA 22313-1450

Date: March 15, 2004

Hogan & Hartson, LLP

500 South Grand Avenue, Suite 1900

Los Angeles, California 90071

Telephone: 213-337-6700

Facsimile: 213-337-6701

Reynaldo Gallardo

Name of person mailing papers

Signature